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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.			
10/751,081	01/02/2004	Joseph J. Schottler	P06702US0	1318			
34082 7:	590 03/18/2005		EXAMINER				
ZARLEY LA	W FIRM P.L.C.	LAM, TUAN THIEU					
CAPITAL SQU 400 LOCUST,		ART UNIT	PAPĖR NUMBER				
	, IA 50309-2350	2816					
			DATE MAIL ED: 03/18/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

						<i>:</i> :		
Office Action Summary			Application	n No.	Applicant(s)			
		10/751,081		SCHOTTLER, JOSEPH J.				
		Examiner		Art Unit				
			Tuan T. Lar		2816			
The MAILI. Period for Reply	NG DATE of this commun	ication appe	ears on the	cover sheet with the c	orrespondence ad	dress		
THE MAILING DA - Extensions of time mater SIX (6) MONTHS - If the period for reply in the period for reply in the period for reply within Any reply received by	STATUTORY PERIOD FOR ATE OF THIS COMMUNITY be available under the provisions of from the mailing date of this community expecified above is less than thirty (3) is specified above, the maximum states at or extended period for reply the Office later than three months a ljustment. See 37 CFR 1.704(b).	CATION. of 37 CFR 1.13 nunication. 0) days, a reply atutory period wi will, by statute,	6(a). In no even within the statute ill apply and will cause the applic	t, however, may a reply be time ory minimum of thirty (30) days expire SIX (6) MONTHS from ation to become ABANDONE	nely filed s will be considered timel the mailing date of this co D (35 U.S.C. § 133).			
Status								
1) 🕅 Responsive	to communication(s) file	od on 18 Oc	toher 2004					
2a) ☐ This action	Responsive to communication(s) filed on <u>18 October 2004</u> . This action is FINAL . 2b)⊠ This action is non-final.							
<u></u>	,—							
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
4) ⊠ Claim(s) <u>1-</u> 4a) Of the a 5) ⊠ Claim(s) <u>1-</u> 6) ⊠ Claim(s) <u>4.</u> 7) ⊠ Claim(s) <u>8-</u>	 ✓ Claim(s) 1-12 is/are pending in the application. ✓ 4a) Of the above claim(s) is/are withdrawn from consideration. ✓ Claim(s) 1-3,5 and 12 is/are allowed. ✓ Claim(s) 4,6 and 7 is/are rejected. ✓ Claim(s) 8-11 is/are objected to. 							
Application Papers								
 9) ☐ The specification is objected to by the Examiner. 10) ☒ The drawing(s) filed on <u>02 January 2004</u> is/are: a) ☒ accepted or b) ☐ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 								
		by the Exa	aminer. Not	e the attached Office	Action or form P1	O-152.		
Priority under 35 U.S	3.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
Attachment(s)								
1) Notice of Reference	s Cited (PTO-892)			4) Interview Summary	(PTO-413)			
Daniel DescriptionDraftspers	on's Patent Drawing Review (P ire Statement(s) (PTO-1449 or			Paper No(s)/Mail Da Notice of Informal P Other:	te)-152)		

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DETAILED ACTION

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Claim Objections

1. Claims 1 and 6 are objected to because of the following informalities: the recitation of "first and second transistor" is suggested to changed to --first and second transistors--. The recitation of "base" in claim 6, line 2; claim 7, line 7 is supposed to be "gate". Appropriate correction is required.

Claim Rejections - 35 USC § 112

- 2. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claim 4 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 4, the recitation of "the first diode" in line 1 lacks proper antecedent basis.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 5. Claims 6-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Bolz et al. (USP 2004/0130379). Figure 3 shows a circuit for driving the gate of NFET (42), said circuit having a

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source voltage passageway (361, 362, 51) adapted to receive the input signal and electrically connected to the NFET, a local positive voltage passageway (output of the charge pump 373, 380) electrically connected to the NFET, a power source (+48V) electrically connected to the NFET providing a positive source to the NFET as called for in claim 6.

6. Regarding claim 7, figure 3 shows first transistor (51) having a threshold voltage adapted to receive input signal (20), a second transistor (353) electrically connected to the first transistor such that when the threshold voltage of the first transistor is not supplied it will deactivates, said second transistor being electrically connected to the base (gate) of the NFET.

Allowable Subject Matter

- 7. Claim 4 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.
- 8. Claims 8-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 9. Claims 1-3, 5 and 12 are presently allowed.

Conclusion

10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P. CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Primary Examiner

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3/15/2005